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REMARKS

The Office Action of 03/24/2006 has been carefully considered. In response thereto, the claims have been amended as set forth above. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

Claims 1, 2, 5, 6, 8, 10 and 12 were rejected as being anticipated by Sutardja. Claims 9, 11 and 13 were rejected as being unpatentable over Sutardja in view of Albon. Claims 1 and 12 have been amended to more clearly distinguish over the cited references. Reconsideration is respectfully requested.

In particular, the claims have been amended to recite that each auxiliary transistor circuit has an input terminal capacitively coupled to the drain of a respective one of the first and second PMOS transistors. Note capacitors CBa and CBb of the figures. No such feature is taught or suggested by the cited references.

Withdrawal of the rejection and allowance of claims 1-13 is respectfully requested.

Respectfully submitted,



Michael J. Ure, Reg. 33,089

Dated: 08/24/2006